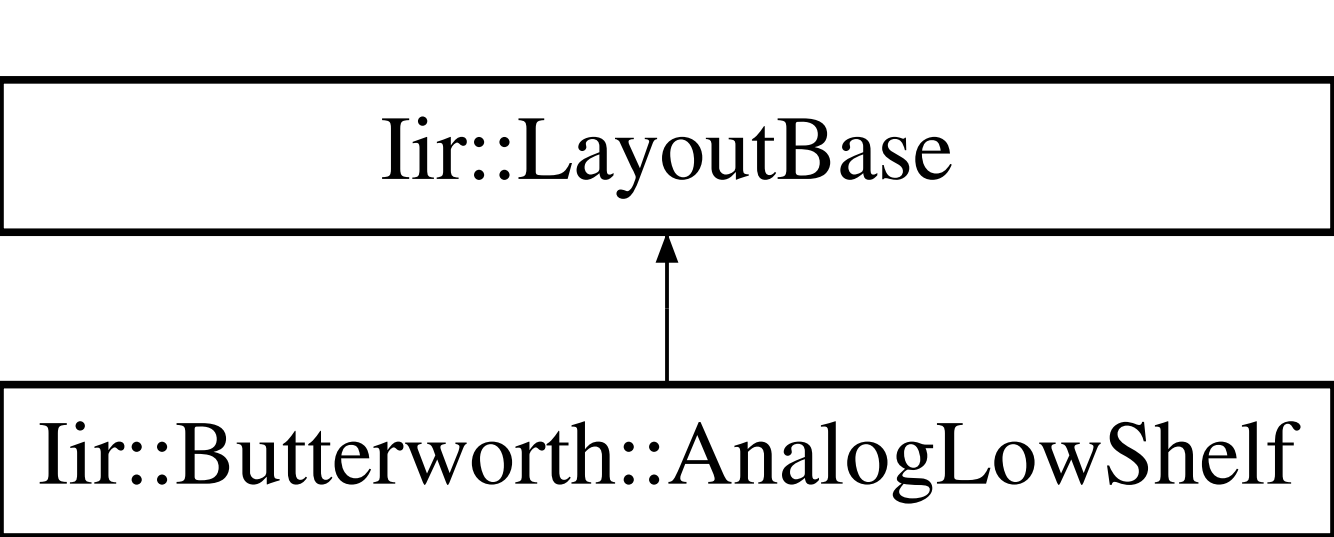


`Iir::LayoutBase`



```
graph BT; A[Iir::Butterworth::AnalogLowShelf] --> B[Iir::LayoutBase]
```

`Iir::Butterworth::AnalogLowShelf`