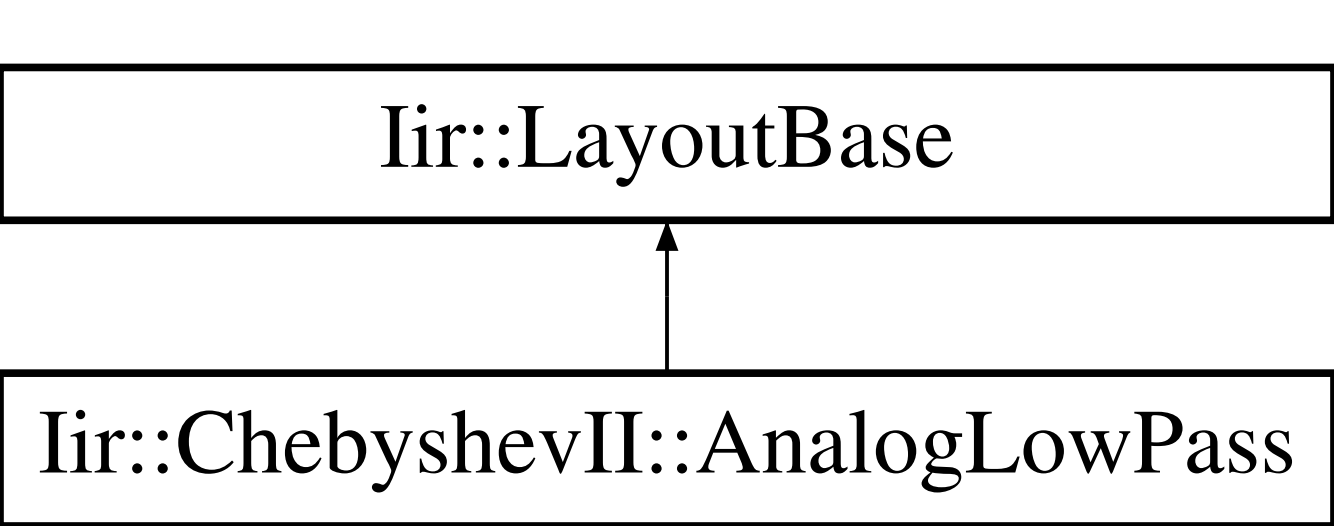


Iir::LayoutBase



```
graph BT; A[Iir::ChebyshevII::AnalogLowPass] --> B[Iir::LayoutBase]
```

Iir::ChebyshevII::AnalogLowPass