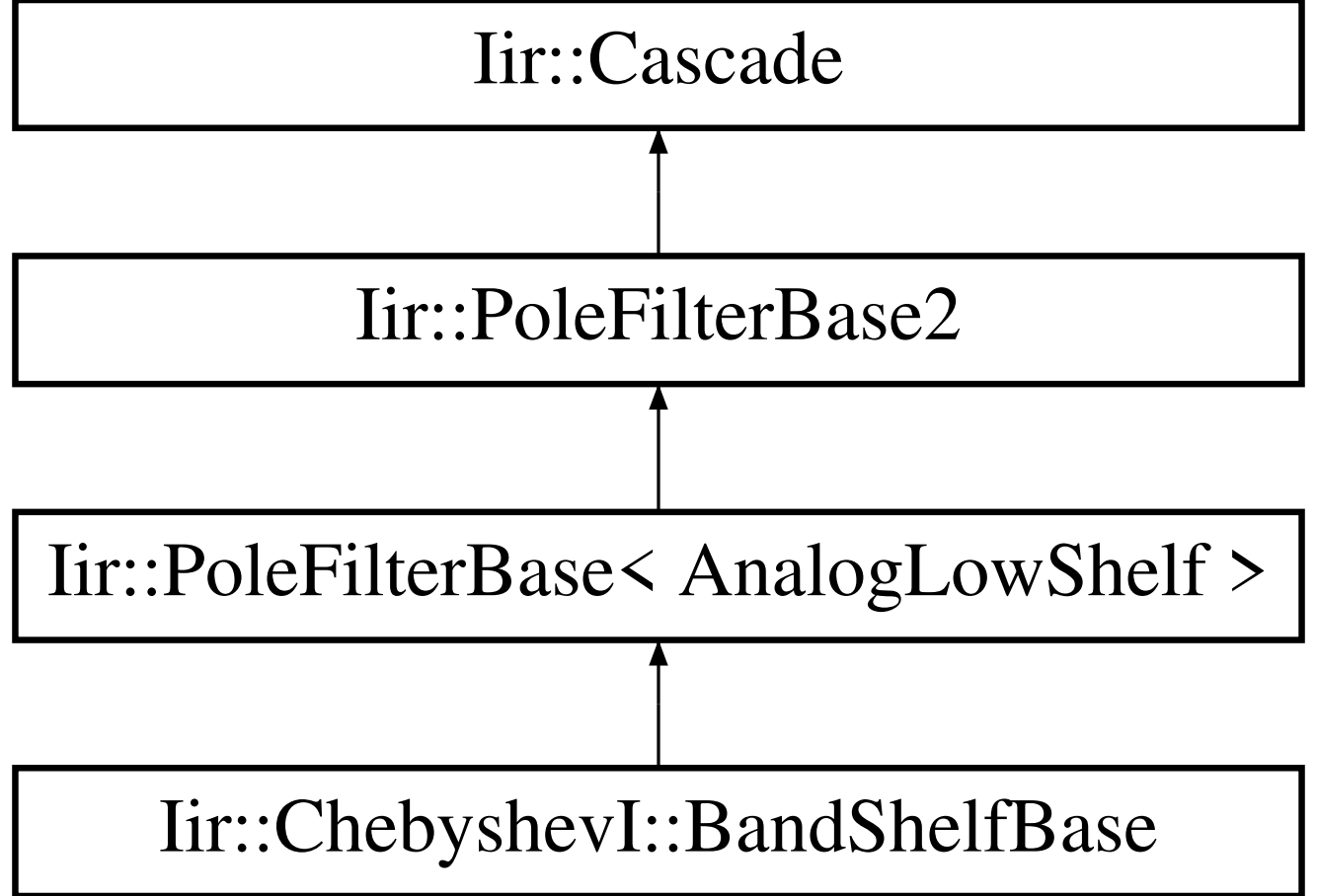


Iir::Cascade



```
graph BT; A[Iir::ChebyshevI::BandShelfBase] --> B[Iir::PoleFilterBase< AnalogLowShelf >]; B --> C[Iir::PoleFilterBase2]; C --> D[Iir::Cascade];
```

Iir::PoleFilterBase2

Iir::PoleFilterBase< AnalogLowShelf >

Iir::ChebyshevI::BandShelfBase