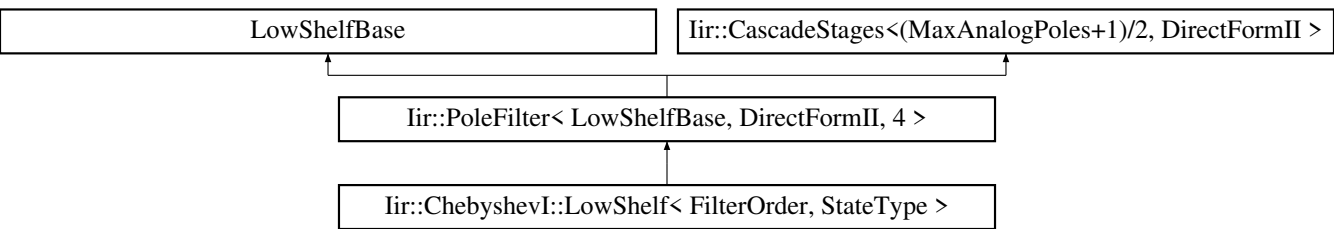
LowShelfBase
Iir::CascadeStages<(MaxAnalogPoles+1)/2, DirectFormII >
Iir::PoleFilter< LowShelfBase, DirectFormII, 4 >
Iir::ChebyshevI::LowShelf< FilterOrder, StateType >